DIPLOMA CURRICULUM OF ELECTRICAL & ELECTRONICS ENGINEERING (SECOND YEAR) (3rd Semester)

(To be implemented from 2025-26)

Prepared by;



National Institute of Technical Teachers' Training & Research Kolkata Block – FC, Sector – III, Salt Lake City, Kolkata – 700106

Vetted by:
Domain experts from Polytechnics of Odisha



State Council for Technical Education & Vocational Training Near Raj Bhawan, Unit-VIII, Bhubaneswar, Odisha

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PROGRAMME TITLE: ELECTRICAL & ELECTRONICS ENGINEERING

SEMESTER - III

				Т	eaching	Schem	е		Evaluatio	n Scheme			
SL.	Category of Course	Code No	Course Title	Pre- requi site	Contac	ct Hours	s/ week	Th	eory	Pra	ectical	Total Marks	
140	Oduse				L	Т	Р	End Exam	Progressive Assessment	End Exam	Progressive Assessment		
1		EEEPC201/ TH:1	Electrical Circuits & Networks		3	0	0	70	30	-	-	100	3
2		EEEPC203/ TH:2	Introduction to Power Generation Systems		3	0	0	70	30	-	-	100	3
3		EEEPC205/ TH:3	Digital Electronics		3	0	0	70	30	-	-	100	3
4		EEEPC207/ TH:4	Electronics Measurement & Instrumentation		3	0	0	70	30	-	-	100	3
5	Programme core	EEEPC209/ TH:5	Signals & Systems		3	0	0	70	30	-	-	100	3
6		EEEPC211/ PR:1	Electrical Circuits & Networks Laboratory		0	0	4	-	-	15	35	50	2
7		EEEPC213/ PR:2	C Programming &WEB Page Design Laboratory		0	0	4	-	-	15	35	50	2
8		EEEPC215/ PR:3	Digital Electronics Laboratory		0	0	4	-	-	15	35	50	2
9		EEEPC217/ PR:4	Electrical and Electronic Measurements Laboratory		0	0	4	-	-	15	35	50	2
10	Summer Internship	SI201	Summer Internship – I*		0	0	0	-	-	15	35	50	2
		TOT	AL		15	0	16	350	150	75	175	750	25

^{*4-}weeks after 2nd Semester

SEMESTER - III COURSES

TH:1- ELECTRIC CIRCUITS & NETWORKS

L	T	P		Correge Coder EEEDC201		
3	0	0		Course Code: EEEPC201		
Total Contact Hours			1 1	Theory Assessment		
Theory		: 45Hrs	Total Marks: 100	End Term Exam 70		
			Total Marks. 100	Progressive Assessment : 30		
Pre Requ	uisite	: Nil				
Credit		3		Category of Course : PC		

RATIONALE: The concept of electrical circuits and networks is very essential for more advanced topics in Electrical and related Engineering programs. This course aims to cover basic circuit concepts, different methods for analyzing large-scale circuits, and applications of these concepts.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Use network theorem for solution of DC network
- Explain the response of R, L, C elements to AC supply
- Calculate various parameters of series and parallel resonance,
- Define and state properties of Laplace Transformation
- Explain operations and characteristics of different kinds of Filter Circuits
- Describe two-port networks

Unit	Topic/Sub Topic	Allotted Time (Hours)
I	Network Theorems in DC Circuits 1.1 Node & Mesh Analysis of Electrical Circuits with simple problem. 1.2 Thevenin's Theorem, Norton's Theorem, Maximum Power transfer Theorem, Superposition Theorem, Millman Theorem, Reciprocity Theorem-Statement, Explanation & applications 1.3 Simple numerical problems above.	5
П	 A. C. Fundamentals & Sinusoidal Steady State Analysis: 2.1 Definitions & explanation of Active & Passive elements. 2.2 Concept of complex impedance, Rectangular & polar form. Simple problems. 2.3 Idea on Apparent, real, and active power. 2.4 Sinusoidal response of a series R-L, R-C, R-L-C circuit 2.5 Sinusoidal response of a parallel R-L, R-C, R-L-C circuit 	8
III	Resonance: 3.1 Introduction to resonance circuits & Resonance tuned circuit, 3.2 Series & Parallel resonance 3.3 Expression for series resonance, Condition for Resonance, Frequency of Resonance, Impedance, Current, Voltage, power, Q Factor and Power Factor of Resonance, Bandwidth in term of Q. Voltage Magnification, Acceptor Circuit. 3.4 Parallel Resonance Condition for Resonance, Frequency of Resonance, Impedance, Current, Voltage, power, Q Factor and Power Factor of Resonance, Bandwidth of resonant circuit / Tank circuit Current magnification, Rejector Circuit, 3.5 Comparisons of Series & Parallel resonance & applications 3.6 Simple problems on above Circuits	8
IV	Passive Filter: 4.1 Idea of Passive & Active Filter, Their relative advantages and disadvantages 4.2 Idea of Fourier Series & frequency spectrum. (concept only) 4.3 Construction, Principle of operation, Characteristics of Low pass, High pass, Band pass & Band stop filter. 4.4 Design of Low pass filter & High pass filter. 4.5 Numerical problems on the above 4.6 Composite filter (concept only).	8
V	Laplace transform and its applications 5.1 Definition & properties of Laplace Transform (LT) 5.2 LT of unit step, impulse, ramp, exponential, sine, cosine, pulse, impulse,	7

	Dirac delta function.	
	5.3 Explanation of Laplace Transform theorems like Differential, integral,	
	Time displacement, initial value & final value	
	5.4 Inverse Laplace Transformation. Simple problem	
	5.5 Application of Laplace transformation in circuit theory	
VI	Two Port Network:	9
	6.1 Idea on Linear & Non linear networks, Unilateral & Bilateral networks	
	6.2 Explanation of Z parameter (Open Circuit Impedance Parameter)	
	6.3 Explanation of Y parameter (Short Circuit Admittance Parameter)	
	6.4 Explanation of h -parameter (Hybrid Parameter)	
	6.5 Interrelation of above parameters	
	6.5 Inter Connection of Two Port Network	
	6.4 Simple problem on above parameters.	

1.	Network Analysis, M. E. Van Valkenburg; Prentice Hall of India
	U T
2.	Circiut Theory (Analysis & Synthesis), A. K. Chakraborty; Dhanpat Rai & Co
3.	Electric Circuit Theory, Chattopadhyay, Rakshit S. Chand &. Co
4.	Network & Systems, D. Roy Choudhury Wiley Eastern Ltd
5.	Networks and Systems, Ashfaq Husain Khanna Publishing House
6.	Engineering Circuit Analysis, W. H. Hayt, J. E. Kemmerly, and S. M. Durbin,
	McGraw Hill

TH:2- INTRODUCTION TO POWER GENERATION SYSTEMS

L	T	P		Course Code: EEEPC203		
Total Cor	ntact Hours	U		Theory Assessment		
Theory		: 45Hrs	Total Marks: 100	End Term Exam 70		
			Total Walks. 100	Progressive Assessment 30		
Pre-Requ	isite	: Nil				
Credit		3		Category of Course: PC		

RATIONALE:

This course concentrates on the field of electric generation systems. It includes thermal power plants: coal, gas/diesel and nuclear-based, large and micro-hydropower plants, solar and biomass based power plants, and wind power plants. After completion of this course, the students will be able to know about economics of power generation and interconnected power system and maintain the efficient operation of various electric power generating plants.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Explain the optimized working of the thermal power plant
- Describe the efficient operation of large and micro hydropower plants.
- Maintain the optimized working of solar and biomass-based power plants
- Discuss the optimized working of wind power plants
- Select the adequate mix of power generation based on economic operation.

Unit No.	Topic/Sub-Topic	Allotted Time (Hours)
I	Thermal Power Plants: Coal, Gas/Diesel and Nuclear-based 1.1 Layout and working of a typical thermal power plant with steam turbines and electric generators 1.2 Properties of conventional fuels used in the energy conversion equipment used in thermal power plants: Coal, Gas, Diesel, Nuclear fuels-fusion and fission action 1.3 Safe Practices and working of various thermal power plants: coal-based, gas-based, diesel-based, and nuclear-based 1.4 Functions of the following types of thermal power plants and their major auxiliaries 1.4.1 Coal fired boilers: fire tube and water tube 1.4.2 Gas/diesel based combustion engines 1.4.3 Types of nuclear reactors: Disposal of nuclear waste and nuclear shielding	10

Large and Micro-Hydropower Plants	9
2.1 Energy conversion process of hydro power plant	
2.2 Classification of hydro power plant: High ,medium and low head	
2.3 Construction and working of hydro turbines used in different types of hydro	
power plant	
2.3.1 High head-Pelton turbine	
 2.3.2 Medium head-Francis turbine 2.3.3 Low head-Kaplan turbine 2.4 Safe Practices for hydro power plants 2.5 Different types of micro-hydro turbines for different heads: Pelton, Francis and Kaplan turbines 2.6 Locations of these different types of large and micro-hydro power plants in India 	
Solar and Biomass based Power Plants	9
 3.1 Solar Map of India: Global solar power radiation 3.2 Solar Power Technology 3.2.1 Concentrated Solar Power (CSP) plants, construction and working of: Power Tower, Parabolic Trough, Parabolic Dish, Fresnel Reflectors 3.2.2 Solar Photovoltaic (PV) power plant: layout, construction, working 	ŕ
3.3 Biomass-based Power Plants 3.3.1 Layout of a Bio-chemical based (e.g. biogas) power plant 3.3.2 Layout of a Thermo-chemical based (e.g. Municipal waste) power plant 3.3.3 Layout of an Agro-chemical based (e.g. bio-diesel) power plant 3.3.4 Features of the solid, liquid and gas biomasses as fuel for biomass power plant	
	2.1 Energy conversion process of hydro power plant 2.2 Classification of hydro power plant: High ,medium and low head 2.3 Construction and working of hydro turbines used in different types of hydro power plant 2.3.1 High head-Pelton turbine 2.3.2 Medium head-Francis turbine 2.3.3 Low head-Kaplan turbine 2.4 Safe Practices for hydro power plants 2.5 Different types of micro-hydro turbines for different heads: Pelton, Francis and Kaplan turbines 2.6 Locations of these different types of large and micro-hydro power plants in India Solar and Biomass based Power Plants 3.1 Solar Map of India: Global solar power radiation 3.2 Solar Power Technology 3.2.1 Concentrated Solar Power (CSP) plants, construction and working of: Power Tower, Parabolic Trough, Parabolic Dish, Fresnel Reflectors 3.2.2 Solar Photovoltaic (PV) power plant: layout, construction, working 3.3 Biomass-based Power Plants 3.3.1 Layout of a Bio-chemical based (e.g. biogas) power plant 3.3.2 Layout of a Thermo-chemical based (e.g. Municipal waste) power plant 3.3.3 Layout of an Agro-chemical based (e.g. bio-diesel) power plant 3.3.4 Features of the solid, liquid and gas biomasses as fuel for biomass

IV	Wind Power Plants	9
	4.1 Wind Map of India: Wind power density in watts per square meter	
	 4.2 Layout of Horizontal axis large wind power plant 4.3 Geared wind power plant 4.4 Direct-drive wind power plant 4.5 Salient Features of electric generators used in large wind power plants 4.5.1 Constant Speed Electric Generators) 4.5.2 Squirrel Cage Induction Generators (SCIG) 4.5.3 Wound Rotor Induction Generator (WRIG) 4.6 Variable Speed Electric Generators 	
	4.6.1 Doubly-fed induction generator (DFIG) 4.6.2 Wound rotor synchronous generator (WRSG) 4.6.3 Permanent magnet synchronous generator (PMSG)	

V	Economics of Power Generation and Interconnected Power System 5.1 Related terms: connected load, firm power, cold reserve, hot reserve, spinning reserve. Base load and peak load plants; Load curve, load duration curve, integrated duration curve 5.2 Cost of generation: Average demand, maximum demand, demand factor, plant capacity factor, plant use factor, diversity factor, load factor and plant load	8
	factor 5.3 Choice of size and number of generator units 5.4 Combined operation of power station 5.5 Causes, Impact and reasons of Grid system fault: State grid, national grid, brownout and blackout; sample blackouts at national and international level.	

1.	Electrical Power Generation by Tanmoy Deb, Khanna Publishing House, Delhi.
2.	Generation of Electrical Energy by B.R. Gupta, S. Chand & Co. New Delhi.
3.	Wind Power Technologies by Rachel, Sthuthi; Earnest, Joshua, PHI Learning, New Delhi.
4.	Solar Photovoltaics: Fundamentals, Technologies and Applications by Chetan Singh Solanki, PHI Learning, New Delhi.
5.	Wind Energy Basics by Gipe Paul, Chelsea Green Publishing Co.
6.	Wind Power Plants and Project Development by Wizelius, Tore, Earnest, Joshua, PHI.
7.	A Course in Electrical Power by J.B. Gupta, S.K. Kataria and Sons, New Delhi.
8.	A Course in Electrical Power by Soni, Gupta, Bhatnagar, Dhanpat Rai and Sons.

TH:3- DIGITAL ELECTRONICS

L	T	P		Course Code: EEEPC205	
3	0	0		Course Code: EEEI C203	
Total Contact Hours			1	Theory Assessment	
Theory		: 45Hrs	Total Marks: 100	End Term Exam 70	
			Total Walks. 100	Progressive Assessment 30	
Pre Requi	isite	: Nil			
		V 1 \ <u>1</u>			
Credit		3		Category of Course : PC	

RATIONALE:

Now a day's digital electronics has become a part of our everyday life. The tremendous power and usefulness of digital electronics can be seen from the wide variety of industrial and consumer products, such as automated industrial machinery, computers, microprocessors, pocket calculators, digital watches, microcontrollers, digital life support machines, real time systems and clocks, TV games, etc. which are based on the principles of digital electronics. The areas of applications of digital electronics have been increasing every day. This subject will very much helpful for student to understand clearly about the developmental concept of digital devices. Through a balanced series of lectures and hands-on laboratory sessions in this course, the student will acquire a solid foundation in digital logic, including gates, binary numbers, flipflops, registers, counters, display devices and applications of Boolean algebra.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Comprehend the Basic Logic Gates and Universal Gates and its functions.
- Explain the laws of Boolean algebra and Standard form of Boolean Expression
- Describe the working principle of Latches and Flip Flops
- Comprehend Shift Registers its circuit diagram, truth tables and timing diagram
- Explain circuit diagrams and the working principles of Counters.
- Design combinational and sequential logic circuits
- Explain various types of memories & differentiate between ROM and RAM

Unit No.	Topic/Sub-Topic		
I	Logic Gates	4	
	1.1 Basic logic gates: OR, AND, and NOT		
	1.1.1 Truth tables		
	1.1.2 Logic symbols		
	1.1.3 Logic voltage levels		
	1.1.4 Logic circuit design examples		
	1.2 Integrated Circuits		
	1.3 NOR, NAND, Exclusive OR, and Exclusive NOR gates.		
	1.4 NOR and NAND gates used as inverters.		
	1.5 Fan-in and fan-out		
	1.6 Termination of unused inputs		
	1.7 AND and OR gates constructed from NAND and NOR gates		

	Boolean Algebra 2.1 Boolean operations (OR, AND, NOT) 2.2 Representation of logic circuits by Boolean expressions. 2.3 Laws of Boolean algebra: 2.3.1 Double inversion: A"=A	
	2.3 Laws of Boolean algebra:	
	2.3 Laws of Boolean algebra:	
	2.3.1 Double inversion: A"=A	
	2.3.1 Double inversion. A -A	
	2.3.2 OR identities: $A+0=A$, $A+1=1$, $A+A=A$, $A+A'=1$	
	2.3.3 AND identities: A.0=0, A.1=A, A.A=A, A.A'=0	
	2.3.4 Cumulative laws: A+B=B+A, A.B=B.A	
	2.3.5 Associative laws:(A+B)+C=A+(B+C), (A.B).C=A.(B.C)	
	2.3.6 Distributive laws: A+(B.C)=(A+B).(A+C), A.(B+C)=A.B+A.C	
	2.3.7 DeMorgan's theorems	
	:(A+B+C+)'=A'.B'.C',(A.B.C)'=A'+B'+C'	
	2.3.8 Applications to logic circuit simplifications and design	
	2.4 Equivalent logic gates	
	2.5 NAND and NOR implementations of logic circuits.	
	2.6 Standard forms of Boolean expressions	
	2.6.1 Sum-of-products (SOP)	
	2.6.2 Product-of-sums (POS)	
	2.7 Karnaugh mapping	
Ш	Combinational Logic Circuits	6
	3.1 Half adder	
	3.2 Full adder	
	3.3 Half Subtractor	
	3.4 Full Subtractor	
	3.5 4 bit adder.	
	3.6 Multiplexer (4:1)	
	3.7 De- multiplexer (1:4)	
	3.8 Decoder	
	3.9 Encoder	
	3.10 Digital comparator (3 Bit)	
	3.11 Seven segment Decoder	

	Latches & Flip-Flops	6
IV	4.1. Basic latches	
	4.1.1 NOR latch	
	4.1.2 NAND latch	
	4.1.3 Example uses of latches	
	4.2. Gated latches	
	4.2.1 Gated S-R latch	
	4.2.2 Gated D-latch	
	4.3. Flip-flops:	
	4.3.1 Master-slave and edge-triggered principles	
	4.3.2 S-R flip-flop	
	4.3.3 D-type flip-flop	
	4.3.4 J-K flip-flop	
	4.3.5 T-type flip-flop	
	4.3.6 Flip-flop timing diagrams	

6.1 Circuit diagram, truth tables, and timing diagrams of Shift Registers 6.2 Serial input shift register 6.3 Serial/parallel load shift register 6.4 Shift register counters 6.4.1. Ring counter 6.4.2. Self-starting ring counter 6.4.3. Johnson counter VII Semiconductor Memories 7.1 Define the terms ROM, RAM, PROM, EPROM. 7.2 Draw a typical memory cell 7.3 Design a small diode matrix ROM to serve as a code converter. 7.4 Design and draw the logic diagram of a specified size memory system 7.5 Operating principle of dynamic memory 7.6 Advantages and disadvantages of dynamic memory vs. static memory 7.7 Difference between dynamic memory vs. static memory VIII Sequential Circuit Design 5	7	Counters	V
5.3 Asynchronous counters and ripple counter 5.4 Synchronous counters 5.5 Decade counter 5.6 Module—n counter and its combinations 5.7. Divide-by-n counters obtained from truncated binary sequences 5.8 Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using J-K flip-flops VI Shift Registers 6.1 Circuit diagram, truth tables, and timing diagrams of Shift Registers 6.2 Serial input shift register 6.3 Serial/parallel load shift register 6.4 Shift register counters 6.4.1. Ring counter 6.4.2. Self-starting ring counter 6.4.3. Johnson counter VII Semiconductor Memories 7.1 Define the terms ROM, RAM, PROM, EPROM. 7.2 Draw a typical memory cell 7.3 Design a small diode matrix ROM to serve as a code converter. 7.4 Design and draw the logic diagram of a specified size memory system 7.5 Operating principle of dynamic memory 7.6 Advantages and disadvantages of dynamic memory vs. static memory 7.7 Difference between dynamic memory vs. static memory VIII Sequential Circuit Design 5.3 Asynchronous counters 5.4 Synchronous counter design using D-type flip-flops 5.5 Asynchronous counter design using D-type flip-flops 5.6 Asynchronous counter design using D-type flip-flops 5.7 Asynchronous counter design using D-type flip-flops 5.8 York properties 5.8 York properties 5.9 Asynchronous counter design using D-type flip-flops 5.9 Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using D-type flip-flops 5.9 Asynchronous counter design using D-type flip-flops 5.9 Asynchronou		5.1 Circuit diagram and working principle of Binary counters	
5.4 Synchronous counters 5.5 Decade counter 5.6 Module—n counter and its combinations 5.7. Divide-by-n counters obtained from truncated binary sequences 5.8. Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using J-K flip-flops 5.9 Synchronous counter design using J-K flip-flops 6.1 Circuit diagram, truth tables, and timing diagrams of Shift Registers 6.2 Serial input shift register 6.3 Serial/parallel load shift register 6.4 Shift register counters 6.4.1. Ring counter 6.4.2. Self-starting ring counter 6.4.3. Johnson counter VII Semiconductor Memories 7.1 Define the terms ROM, RAM, PROM, EPROM. 7.2 Draw a typical memory cell 7.3 Design a small diode matrix ROM to serve as a code converter. 7.4 Design and draw the logic diagram of a specified size memory system 7.5 Operating principle of dynamic memory 7.6 Advantages and disadvantages of dynamic memory vs. static memory 7.7 Difference between dynamic memory vs. static memory VIII Sequential Circuit Design 5.4 Synchronous counter floor truncated binary sequences 5.5 Synchronous counter design using D-type flip-flops 5.6 Module—flops 5.7 Divide-by-n counter design using D-type flip-flops 5.7 Divide-by-n counter design using D-type flip-flops 5.8 Synchronous counter design using D-type flip-flops 5.7 Divide-by-n counter design using D-type flip-flops 5.8 Synchronous counter design using D-type flip-flops 5.7 Divide-by-n counter design using D-type flip-flops 5.8 Synchronous counter design using D-type flip-flops 5.7 Divide-by-n counter design using D-type flip-flops 5.8 Sequential circuit Design 5.7 Divide-by-n counter design using D-type flip-flops 5.8 Synchronous counter design using D-type flip-flops 5.8 Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using D-type flip-flops 5.		5.2 up-down counter (circuits, truth tables, and timing diagrams)	
5.5 Decade counter 5.6 Module—n counter and its combinations 5.7. Divide-by-n counters obtained from truncated binary sequences 5.8. Synchronous counter design using D-type flip-flops 5.9 Synchronous counter design using J-K flip-flops VI Shift Registers 6.1 Circuit diagram, truth tables, and timing diagrams of Shift Registers 6.2 Serial input shift register 6.3 Serial/parallel load shift register 6.4 Shift register counters 6.4.1. Ring counter 6.4.2. Self-starting ring counter 6.4.3. Johnson counter VII Semiconductor Memories 7.1 Define the terms ROM, RAM, PROM, EPROM. 7.2 Draw a typical memory cell 7.3 Design a small diode matrix ROM to serve as a code converter. 7.4 Design and draw the logic diagram of a specified size memory system 7.5 Operating principle of dynamic memory 7.6 Advantages and disadvantages of dynamic memory vs. static memory 7.7 Difference between dynamic memory vs. static memory VIII Sequential Circuit Design 5.5		5.3 Asynchronous counters and ripple counter	
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VIII Sequential Circuit Design 5		7.6 Advantages and disadvantages of dynamic memory vs. static memory	
		7.7 Difference between dynamic memory vs. static memory	
	5	Sequential Circuit Design	VIII
8.1 Combinational vs. Sequential circuits	-	8.1 Combinational vs. Sequential circuits	
8.2 Adder, Subtractor, decoder, multiplexer, de-multiplexer, and comparator			
8.3. Finite state machines- Concept only			

1.	Modern Digital Electronics by R P Jain
2.	Electronic Devices And Circuits by S Salivahanan, N Suresh Kumar, and A Vallavaraj
3.	Fundamental of Digital Electronics by Ananda Kumar-PHI Publication
4.	Digital logic and computer design by M. Morris Mano
5.	Digital Electronics by Dr. R. S. Sedha, S. chand
6.	Digital Electronics & Microprocessor - problem & solutions by R.P. Jain ,TMH

TH:4- ELECTRONICS MEASUREMENT & INSTRUMENTATION

L 3	T 0	P 0		Course Code: EEEPC207
Total Contact Hours			Theory Assessment	
Theory		: 45Hrs	Total Marks: 100	End Term Exam 70
			Total Walks. 100	Progressive Assessment 30
Pre Requi	isite	: Nil		
Credit		3		Category of Course: PC

RATIONALE:

Electronics Measurement & Instrumentation serves not only in science & technology but also spread over to all branch of engineering. Measurement is basically used to monitor a process or operation as well as controlling the process. Usually instruments which are used to measure any quantity, are known as measuring instruments. If the instruments can measure the basic electrical quantities, such as voltage and current are known as basic measuring instruments. The basic concept and working principle of measuring instruments are incorporated in this subject. The Analog & Digital types of Instruments are also discussed in this subject.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Explain the Qualities of Measurement
- Describe the operating principle of Indicating Instruments
- Discuss about Digital Instruments and Oscilloscope.
- Explain different types of Bridges and working principles.
- Discuss about Transducers & Sensors, Signal Generator & Wave Analyzer and measurements using Electronics Devices

Unit No.	Topic/Sub-Topic				
I	Qualities of Measurement				
	1.1 Discuss the Static Characteristics				
	1.2 Accuracy, sensitivity, reproducibility and static error of instruments				
	1.3 Dynamic characteristics and speed of instruments.				
	1-4 Errors of an instrument				
II	Indicating Instruments	8			
	2.1 Introduction				
	2.2 Types of Indicating Instruments				
	2.3 Basic operating principle of Indicating Instruments				
	2.4 Working principle of permanent magnetic moving coil Instruments				
	2.5 Working principle of Moving Iron Instrument				
	2.6 Basic principle of operation of DC Ammeter and Multi range Ammeter				
	2.7 Basic principle of operation of AC Ammeter and Multi range Ammeter				
	2.8 Basic principle of operation of DC Voltmeter and its applications				
	2.9 Basic principle of operation of AC Voltmeter and its application				
	2.10 Basic principle of Ohm Meter (Series & Shunt type)				
	2.11 Basic principle of Analog Multimeter and its types & applications				
	2.12 Operation of Q meter and its essentials				
III	Digital Instruments				
	3.1 Principle of operation of Ramp type Digital Voltmeter & applications	6			
	3.2 Operation of display of Digital Multimeter & Resolution and Sensitivity				
	3.3 Basic Operating principle of Digital Multimeter, its types & applications				
	3.4 Basic Operating principle of Digital Frequency Meter				
	3.5 Digital Measurement of Time				
	3.6 Measurement of Frequency				
	3.7 Operating principle of Digital Tachometer				
	3.8 LCR meter & its working principle				
IV	Oscilloscope	6			
-,	4.1 Basic Operating principle of Oscilloscope & its Block Diagram				
	4.2 Basic Operating principle of Dual Trace Oscilloscope & its specification				
	4.3 CRO Measurements				
	4.4 Lissajous figures				
	4.4 Applications of Oscilloscope in measurement of Voltage and frequency				
	4.5 Basic Operating principle of Digital Storage Oscilloscope				
	4.6 Basic Operating principle of High frequency Oscilloscope				
V	Bridges	6			
•	5.1 Types of Bridges (DC & AC Bridges)				
	5.2 DC Bridges (Measurement of Resistance by Wheatstone's Bridge)				
	5.3 AC bridges (Measurement of inductance by Maxwell's Bridge & Hay's				
	Bridge)				
	5.3 Measurement of capacitance by Schering's Bridge & DeSauty Bridge				
	5.5 Working principle of Q meter its circuit diagram & measurement of Low				
	impedance				
	5.6 Measurement of frequency				
	5.7 LCR Meter & its measurements.				
VI	Transducers & Sensors	8			
V I	6.1 Define Transducer and Sensor	0			
	6.2 Type of Transducer				

6.3 Parameters and advantages of Transducer	
6.4 Working principle of Strain Gauges,	
6.5 Define Strain Gauge (No mathematical Derivation)	
6.6 Working principle of LVDT	
6.7 Working principle of capacitive transducers (pressure)	
6.8 Working principle of Load Cell (Pressure Cell)	
6.9 Working principle of Temperature Transducer (RTD, Optical Pyrometer,	
Thermocouple, and Thermister)	
6.10 Working principle of Current transducer.	
6.11 Working principle of Proximity & Light sensors.	
Signal Generator, Wave Analyser & DAS	5
7.1 General aspect & classification of Signal generators	
7.2 Working principle of AF Sine and Square wave generator	
7.3 Working principle of the Function Generator	
7.4 Function of basic Wave Analyser and Spectrum Analyser	
7.5 Basic concept of Data Acquisition System (DAS)	
	6.4 Working principle of Strain Gauges, 6.5 Define Strain Gauge (No mathematical Derivation) 6.6 Working principle of LVDT 6.7 Working principle of capacitive transducers (pressure) 6.8 Working principle of Load Cell (Pressure Cell) 6.9 Working principle of Temperature Transducer (RTD, Optical Pyrometer, Thermocouple, and Thermister) 6.10 Working principle of Current transducer. 6.11 Working principle of Proximity & Light sensors. Signal Generator, Wave Analyser & DAS 7.1 General aspect & classification of Signal generators 7.2 Working principle of AF Sine and Square wave generator 7.3 Working principle of the Function Generator 7.4 Function of basic Wave Analyser and Spectrum Analyser

1.	Electronic Instrumentation by H S Kalsi, McGraw Hill
2.	Electrical Measurement Instrumentation by J.B.Gupta, Katson books
3.	Electrical & Electronics Measurement & Instrumentation by A K Sawheny
4.	Electrical and Electronic Measurements and Instrumentation by R.K.Rajput, S Chand

TH:5- SIGNAL & SYSTEMS

L	T	P		Course Code: EEEPC209
3	0	0		
Total Cor	tact Hours			Theory Assessment
Theory		: 45Hrs	Total Marks: 100	End Term Exam 70
			Total Walks. 100	Progressive Assessment 30
Pre Requ	isite	: Nil		,
Credit		3		Category of Course : PC

RATIONALE:

A sound understanding of the basic concept of modern digital signal processing, communication, and control systems is necessary for all students of Electronics and Communication Engineering. The goal of the course is to teach students how to analyze and represent signals and systems, and to understand how systems manipulate signals to perform a function. The fundamental principles of signals and system analysis is introduced in this course.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Identify the sources of signals, and systems in real life.
- Characterize different types of signals and systems.
- Represent continuous-time and discrete-time systems in different mathematical forms.
- Analyze system behavior using time and frequency domain techniques.

Unit No.	Topic/Sub-Topic	Allotted Time (Hours)
I	Introduction to Signals and Systems 1.1 Signals and systems as seen in everyday life 1.2 Signals and systems in various branches of engineering 1.3 Electrical, mechanical, hydraulic, thermal, biomedical signals and systems as examples 1.4 Extracting the common essence and requirements of signal and system	5

II	Formalizing signals	8
	2.1 Energy and power signals	
	2.2 Signal properties	
	2.2.1 Periodicity	
	2.2.2 Absolute integrability	
	2.2.3 Determinism and stochastic character	
	2.3 Some special signals of importance	
	2.3.1 The unit step	
	2.3.2 The unit impulse	
	2.3.3 The sinusoid	
	2.3.4 The complex exponential	
	2.4 some special time-limited signals	
	2.4.1 Continuous and discrete time signals,	
	2.4.2 Continuous and discrete amplitude signals.	
	2.5 Formalizing systems- system properties	
	2.5.1 Linearity	
	2.5.2 Additivity and homogeneity	
	2.5.3 Shift-invariance	
	2.5.4 Causality	
	2.5.5 Stability	
	2.5.6 Reliability	
III	Continuous time and discrete time Systems	6
***	3.1 Linear shift-invariant (LSI) systems in detail	
	3.2 The impulse response and step response	
	3.3 Convolution	
	3.4 Input-output behavior with aperiodic convergent inputs	
	3.5 Cascade interconnections	
	3.6 Characterization of causality and stability of linear shift-invariant systems	
	3.7 System representation through differential equations and difference	
	equations	
	equations	
IV	Periodic and semi-periodic inputs to an LSI system	8
	4.1 The notion of a frequency response and its relation to the impulse response,	
	4.2 Fourier series representation	
	4.3 The Fourier Transform	
	4.4 Convolution/multiplication and their effect in the frequency domain,	
	magnitude and phase response, Fourier domain duality.	
	4.5 The Discrete-Time Fourier Transform (DTFT)	
	4.6 The Discrete Fourier Transform (DFT)	
	4.7 Parseval's Theorem	
	4.8 The idea of signal space and Orthogonal bases of signals.	
T 7		
V	Laplace Transform for continuous time signals and systems 5.1 The notion of Eigen functions of LSI systems	8
	5.1 The notion of Eigen functions of Est systems	
	•	

	5.2 A basis of Eigen functions	
	5.3 Region of convergence	
	5.4 System functions	
	5.5 Poles and zeros of system functions and signals	
	5.6 Laplace domain analysis	
	5.7 Solution to differential equations and system behavior	
	5.8 Generalization of Parseval's Theorem	
VI	System realization	7
	6.1 System realization through block-diagram representation and system	
	interconnection	
	6.2 State-space analysis and multi-input, multi-output representation. 6.3 The	
	state-transition matrix and its role.	
	6.4 The Sampling Theorem and its implications	
	6.4.1 Spectra of sampled signals.	
	6.5 Reconstruction:	
	6.5.1 Ideal interpolator	
	6.5.2 Zero-order hold	
	6.5.3 First-order hold	
	6.6 Aliasing and its effects.	
	6.7 Relation between continuous and discrete time systems.	
VII	Applications of signal and system theory	5
	7.1 Modulation for communication and filtering	
	7.2 Time-frequency representation and the uncertainty principle	
	7.3 Short-time Fourier Transforms and wavelet transforms.	

1.	R. Anand, Signals and Systems, Khanna Publishing House, 2019
2.	B.P. Lathi, "Signal Processing and Linear Systems", Oxford University Press, 1998.
3.	A.V. Oppenheim, A.S. Willsky and I.T. Young, "Signals and Systems", Prentice Hall
4.	R.F. Ziemer, W.H. Tranter and D.R. Fannin, "Signals and Systems - Continuous and Discrete", 4th edition, Prentice Hall, 1998
5.	Simon Haykin, Barry van Veen, "Signals and Systems", John Wiley and Sons (Asia) Private Limited, c1998.
6.	Douglas K. Lindner, "Introduction to Signals and Systems", Mc-Graw Hill International Edition: c1999
7.	P. Ramesh Babu, Signals and Systems, SCITECH

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PR:1- ELECTRIC CIRCUITS & NETWORKS LAB

L	T	P		Course Coder EEED	C211
0	0	4	Course Code: EEEPC	C211	
Total Contact Hours				Laboratory Assessm	nent
Practical		: 60Hrs	Total Marks: 50	End Term Exam	15
			Total Mai Ks. Su	Progressive Assessment	35
Pre Requ	isite	: NIL			
Credit		2		Category of Course : PC	

RATIONALE: This course aims to cover basic skill development on circuits, different experimental methods for analyzing large-scale circuits, and real time applications of these concepts.

LEARNING OUTCOMES: After completion of the Lab the students will be able to

- Use network theorems for solution of DC network
- Analyze charging & discharging of RC circuit with CRO
- Design series and parallel resonance circuits with a particular cut of frequency and to plot frequency response
- Design filters and plot frequency response.
- Simulate circuits for various applications.

Sl No	List of Experiments
I	Verification of-
	· Superposition theorem.
	· Thevenin's theorem.
	· Norton's theorem.
	· Maximum power transfer theorem.
II	Use voltmeter, ammeter to determine current through the given branch of an electric network by applying mesh analysis.
III	Use voltmeter, ammeter to determine current through the given branch of a electric
	network by applying node analysis.
IV	Observe an AC wave form on CRO and calculate its average & RMS values, frequency,
	time period
V	Analysis of charging & discharging of RC circuit with CRO (calculation of time constant,
	rise time etc.).
VI	Design of series resonance circuit with a particular cut of frequency and to plot frequency
	response
VII	Design of parallel resonance circuit with a particular cut of frequency and to plot
VII	
	frequency response
VIII	Designing (considering cut-off frequency) Low pass filter and to plot frequency response
IX	Designing of (considering cut-off frequency) High pass filter to plot frequency response
X	Simulate the above circuits using circuit simulation software.

PR:2- CPROGRAMMING & WEB PAGE DESIGN LAB

L 0	T 0	P 4		Course Code: EEEP	C 213
Total Contact Hours				Laboratory Assessm	ent
Practical		: 60Hrs	Total Marks: 50	End Term Exam	15
			Total Walks. 30	Progressive Assessment	35
Pre Requ	isite	: NIL			
Credit		2		Category of Course : PC	

RATIONALE:

C is a procedural programming language initially developed by Dennis Ritchie at Bell Laboratories of AT&T Labs. It was mainly developed as a system programming language to write the UNIX operating system. Web design encompasses many different skills and disciplines in the production and maintenance of websites. The different areas of web design include web graphic design; interface design; authoring, including standardized code and proprietary software; user experience design; and search engine optimization. This lab will provide training and guidance to students in Information Technology and Computers and will provide an environment conducive for E-learning and research.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Write C programs for solving problems.
- Impart the concepts like looping, array, functions, pointers, file, and structure during writing a program.
- Design active web page as per today's need.

Sl No	List of Experiments
	Assignments to write C Program
I	To display all even numbers from 1-100.
II	To perform addition of 1-100 numbers
III	To find smallest / largest number from array elements
IV	To sort array elements in ascending / descending order.
V	To calculate addition / subtraction of 2 dimensional matrix.
VI	To calculate multiplication of 2 dimensional matrix
VII	To calculate factorial of any given number using recursion
VIII	To calculate area of circle using function.
	Assignments for Web Page Design
IX	Browse different search engines and search different topics & Crete an e-mail account & use attachment facility
X	Design a general webpage Design a general website

XI	Design multimedia page which includes Text, Audio, video, images, Animation	
XII	Design a general website & multimedia page which includes Upload the website	
	on college server & load website on public internet.	
XIII	Designs Web page and apply some block level tags and some text level tags &	
	Include Horizontal Rules and special characters in a Web page.	
XIV	Design Web page and include different lists & various links in a Web page &	
	Include images with different alignments and wrapped text in Web page. Also	
	include image as a link in the Web page.	
XV	Design a web page with background image, different text colour for different	
	Paragraphs, and set colors for links, active links and visited links.	
XVI	Create HTML table, format contents in table cells and span the rows and columns	
Atleast t	Atleast twelve(12) experiments to be performed by each student	

1.	Programming in C by Balgurusamy, Tata MGH
2.	Web page Design -TMG/Oxford/BPB publications

PR:3- DIGITAL ELECTRONICS LAB

L	T	P		Course Code: EEEPC215
0	0	4		
Total Contact Hours				Laboratory Assessment
Practical		: 60Hrs	Total Marks: 50	End Term Exam 15
			Total Walks. 30	Progressive Assessment 35
Pre Requi	isite	: NIL		·
Credit		2		Category of Course : PC

RATIONALE:

This course provides students a structured approach to learning the principles and practical applications of digital electronics. Through hands-on laboratory sessions, the student will acquire a solid foundation in digital logic, including gates, binary numbers, flip-flops, registers, counters, display devices and applications of Boolean algebra. Various digital ICs are discussed in this course. This lab also includes combinational logic & sequential logic circuits and its implementations.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Identify the pin description of digital IC's.
- Implement Arithmetic logic circuits using digital IC's.
- Develop combinational circuits using digital IC's.
- Apply concept of universal logic gates for digital circuit designing.
- Examine the behaviour of sequential circuits using digital IC's.

Sl No	List of Experiments
I	To verify the truth tables for all logic fates – NOT OR AND NAND NOR
	XOR XNOR using CMOS Logic gates and TTL Logic Gates
II	Implement and realize Boolean Expressions with Logic Gates
III	Implement Half Adder, Full Adder, Half Subtractor, Full subtractor using
	ICs
IV	Implement parallel and serial full-adder using ICs
V	Design and development of Multiplexer and De-multiplexer using multiplexer
	ICs
VI	Verification of the function of SR,D, JK and T Flip Flops
VII	Design controlled shift registers
VIII	Construct a Single digit Decade Counter (0-9) with 7 segment display
IX	To design a programmable Up-Down Counter with a 7 segment display
X	Study of different memory ICs
XI	Study Digital- to – Analog and Analog to Digital Converters
XII	Simulate in Software (such as PSpice) an Analog to Digital Converter
XIII	Simulate in Software (such as PSpice) an Digital to Analog Converter
Atleast t	welve(12) experiments to be performed by each student

1.	'Modern Digital Electronics' R P Jain
2.	'Electronic Devices And Circuits' S Salivahanan, N Suresh Kumar, A Vallavaraj

PR:4- ELECTRICAL & ELECTRONICS MEASUREMENT LAB

L 0	T 0	P 4		Course Code: EEEPC217	
Total Contact Hours			1	Laboratory Assessment	
Practical		: 60Hrs	Total Marks: 50	End Term Exam	15
			Total Walks. 30	Progressive Assessment	35
Pre Requisite : NIL]		
Credit		2		Category of Course : PC	

RATIONALE:

Modern electronics measurement and automated instrumentation system is an emerging field, used for data sensing, acquisition, transmission, analysis and control in various practical applications in industry. Analog and digital instruments are mainly used to measure different process control parameters. The physical quantities/parameters are being converted into electrical signal with the help of various types of sensors and transducers and also used to maintain electronic control and automation system. This lab aims to help students to better understand measuring devices and their operating principles, Measurement of Circuit Parameters, Power, Power Factor, Phase Angle, Frequency and Time, DC and AC Bridges, Characteristics of Discrete and Integrated Devices, Digital Instrumentation, Transducers.

LEARNING OUTCOMES:

After completion of the course, the students will be able to

- Measurement of Current and Voltages by Low range ammeter and voltmeter respectively with shunt and multiplier.
- Construct Bridges to measure R, L, & C.
- Observe the wave forms of different frequency by using Function generator and draw its diagram. & measure the amplitude and frequency using dual trace CRO.
- Measure the unknown frequency and phase angle using CRO by lissajous pattern.
- Measure the different parameters using Transducer.

Sl No	List of Experiments				
I	Study of construction and calibration of moving coil and moving iron instruments				
II	Study of static and dynamic characteristic of PMMC & moving iron instruments				
III	Study of Resolution and sensitivity of Digital Instrument				
IV	Measurement of Current and Voltages by Low range ammeter and voltmeter respectively with shunt and multiplier				
V	Observation of wave forms of different frequency by using Function generator and draw its diagram measure the amplitude and frequency & calculates average & R.M.S. Values, frequency, Time Periods using CRO				
VI	Measurement of the unknown frequency and phase angle using CRO by Lissajous figure.				
VII	Measurement of resistance using Wheatstone's Bridge				
VIII	Measurement of the inductance by Maxwell's Bridge &Hay's Bridge				

SUMMER INTERNSHIP-I

L 0	T 0	P 0		Course Code: SI2	01
Total Co	ntact Hou	rs		Summer Internship Assessment	
				End Term Exam	15
			Total Marks: 50	Progressive Assessment	: 35
Pre Requ	uisite	: NIL			
Credit		2		Category of Course : Summer Internship	

Duration: 4-weeks after 2nd Semester

RATIONALE

Summer Internship - I is to offer a structured and practical learning experience that prepares individuals for their future careers, helps them make informed career choices, and equips them with the skills and knowledge necessary to succeed in their chosen field. This course provides opportunities to students for hands-on industry experience.

LEARNING OUTCOMES

After completion of the course, the students will be able to:

- Apply theoretical knowledge gained in their academic coursework to real-world situations.
- Enhance specific skills relevant to their field.
- Gain hands-on experience in a professional network by interacting with mentors and industry professionals.
- Manage time effectively.
- Clarify career goals.

DETAILED COURSE CONTENTS

SUGGESTED ACTIVITIES:

I Orientation:

- o Introduction to the organization's mission, values, and culture.
- o Familiarization with workplace policies, procedures, and safety guidelines.
- o Orientation to the team and organizational structure.

II Project-Based Learning:

- o Description of the main project or tasks the intern will be working on during the internship.
- Detailed project goals and objectives.
- Training and guidance on project-specific tools, technologies, or methodologies.

III Technical and Skill Development:

- Training sessions or workshops to enhance technical skills relevant to the internship role (e.g., programming languages, software tools, laboratory techniques).
- Soft skills development, including communication, teamwork, problem solving, and time management

IV Mentorship and Supervision:

- Regular meetings with a designated mentor or supervisor for guidance, feedback, and support.
- o Mentorship objectives and expectations.

V Professional Development:

- o Sessions on professional etiquette, networking, and building a personal brand
- o Resume writing and interview preparation workshops.

VI Industry and Field-Specific Knowledge:

- Lectures, seminars, or presentations on industry trends, best practices, and emerging technologies.
- o Guest speakers from the field to share insights and experiences.

VII Reporting and Documentation:

- o Training on how to document project progress, results, and findings.
- o Practice in creating reports, presentations, or other deliverables.

VIII Ethics and Professionalism:

- o Discussions on ethical considerations within the field.
- o Scenarios and case studies related to ethical decision-making

IX Feedback and Evaluation:

- o Regular performance evaluations and feedback sessions.
- o Self-assessment and goal-setting exercises.

X Networking and Industry Exposure:

- Opportunities to attend industry conferences, webinars, or networking events.
- Encouragement to connect with professionals in the field.

NOTE

As per AICTE guidelines, in Summer Internship-I, students are required to be involved in Inter/ Intra Institutional Activities viz;

- Training with higher Institutions;
- Soft skill training organized by Training and Placement Cell of the respective institutions;
- contribution at incubation/innovation/entrepreneurship cell of the institute;
- participation in conferences/ workshops/ competitions etc.;
- Learning at Departmental Lab/ Tinkering Lab/ Institutional workshop;
- Working for consultancy/ research project within the institutes and
- Participation in all the activities of Institute's Innovation Council for eg: IPR workshop/Leadership Talks/ Idea/ Design/ Innovation/ Business Completion/ Technical Expos etc.